

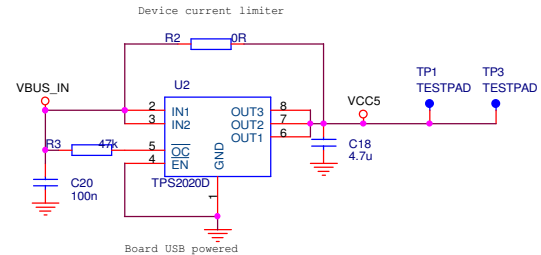
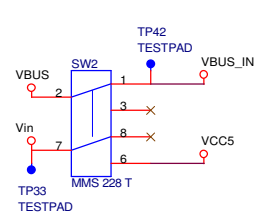
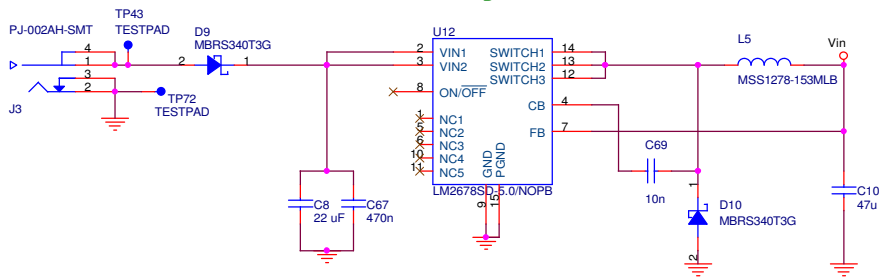
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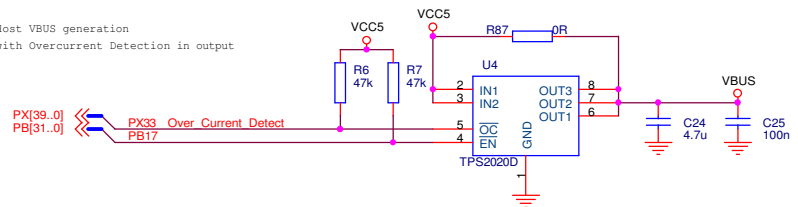
**ATMEL**

Title	EVK1100	
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### 5V Regulator

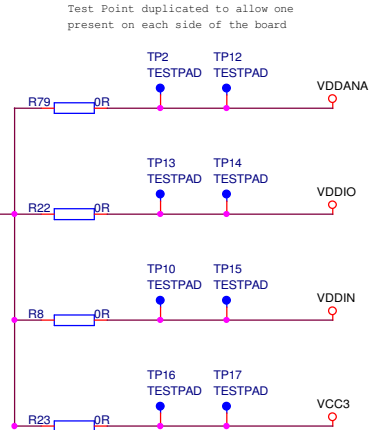
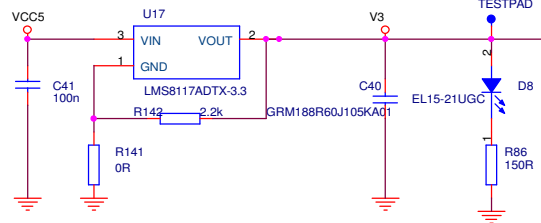


Host VBUS generation with Overcurrent Detection in output

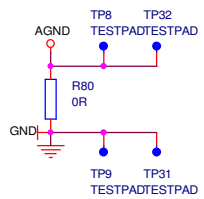


Shall only be added if TPS2020B

### 3.3V Regulator




VDDANA intends to provide a clean 3.3V power supply for analog blocks  
 VDDIO intends to provide a clean 3.3V power supply for ATUC3A0512 buffers  
 VDDIN intends to provide a clean 3.3V power supply for ATUC3A0512 internal regulator  
 VCC3 intends to provide a clean 3.3V power supply for all peripherals



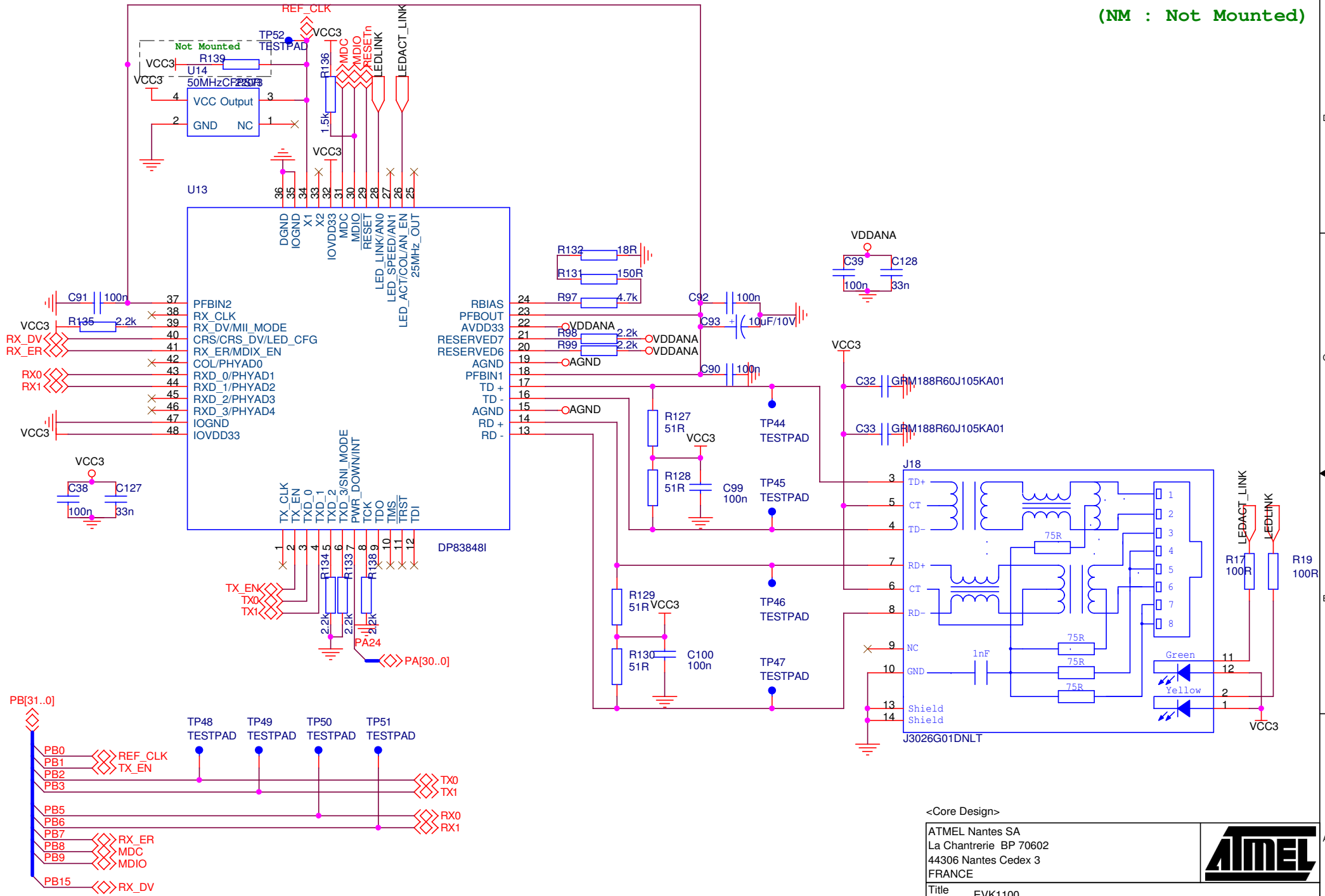
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(NM : Not Mounted)



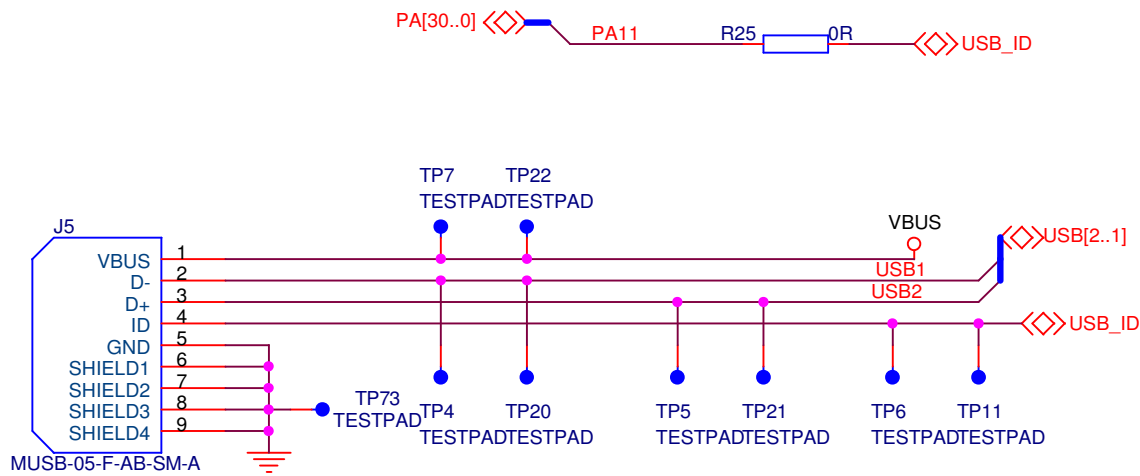
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(NM : Not Mounted)



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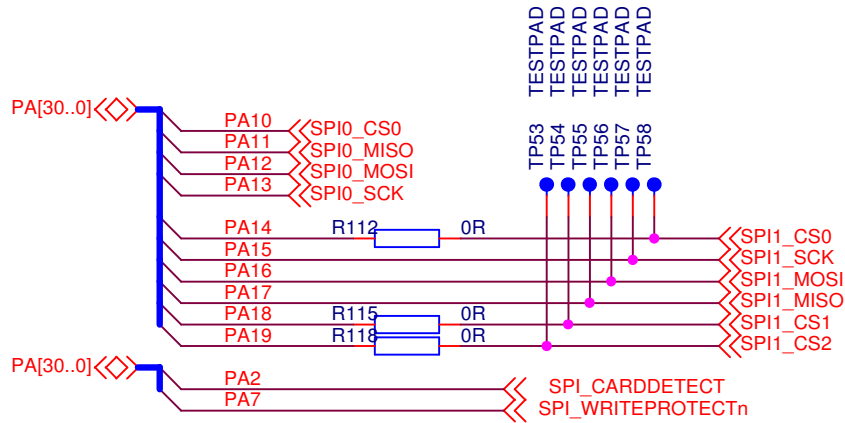
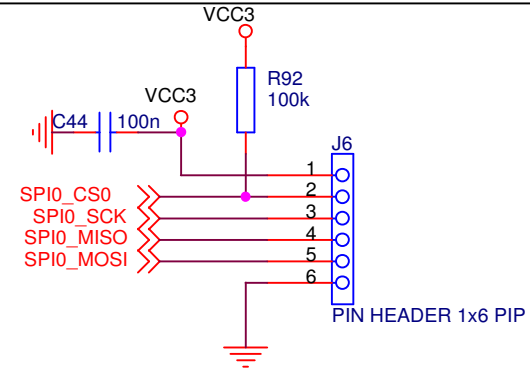
Title EVK1100

Size A Document Number USB Rev C

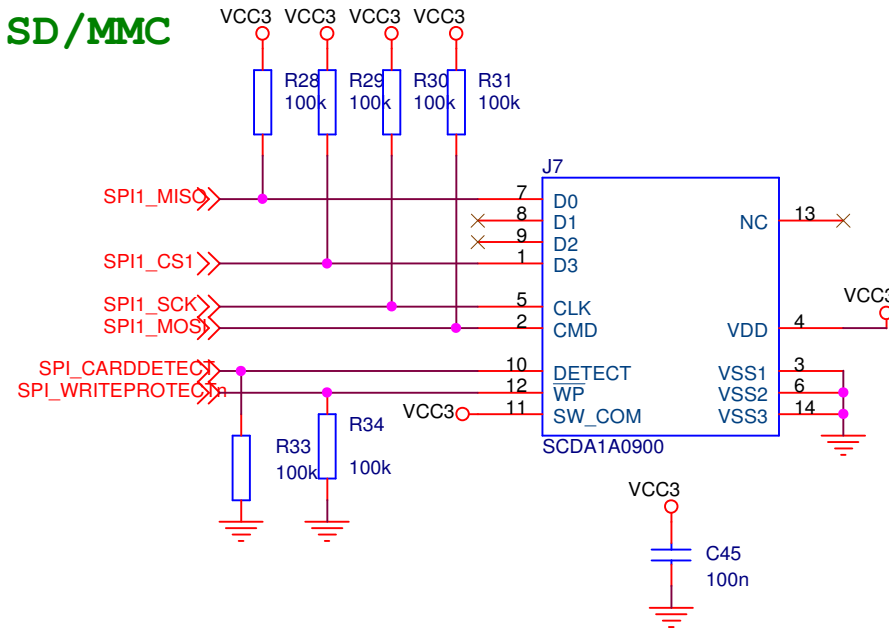
Date: Tuesday, December 18, 2007 Sheet 4 of 13

(NM : Not Mounted)

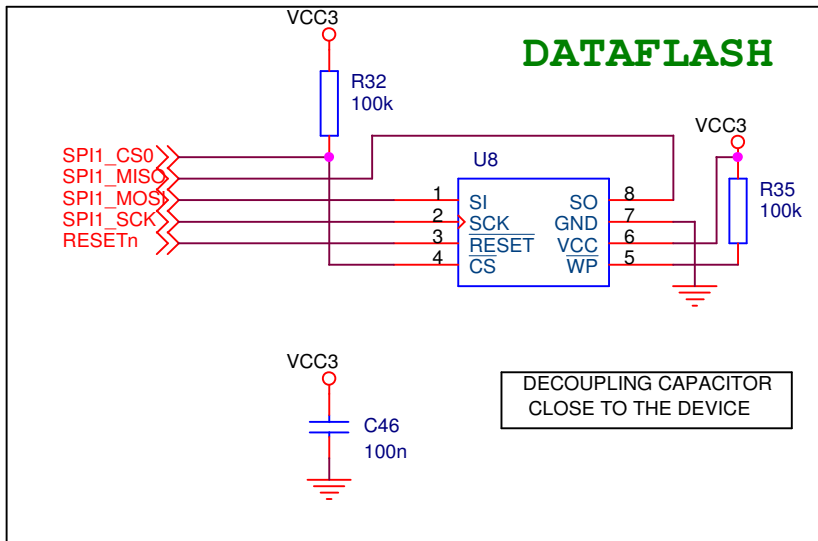
### SPI



### SD/MMC



### DATAFLASH



DECOUPLING CAPACITOR  
CLOSE TO THE DEVICE

<Core Design>

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Title EVK1100

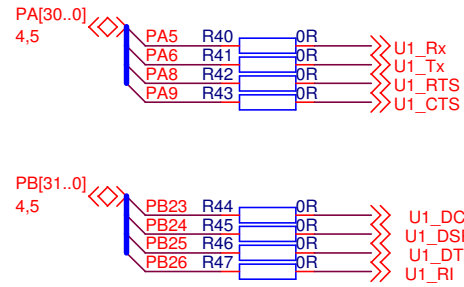
Size A Document Number

SPI

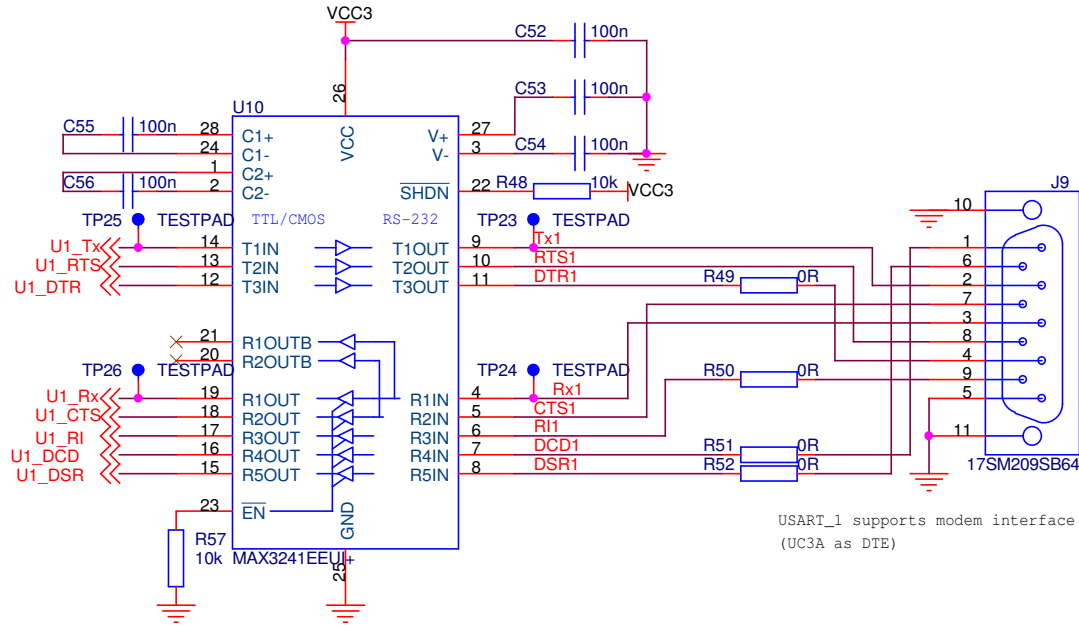
Rev C

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### USART 1

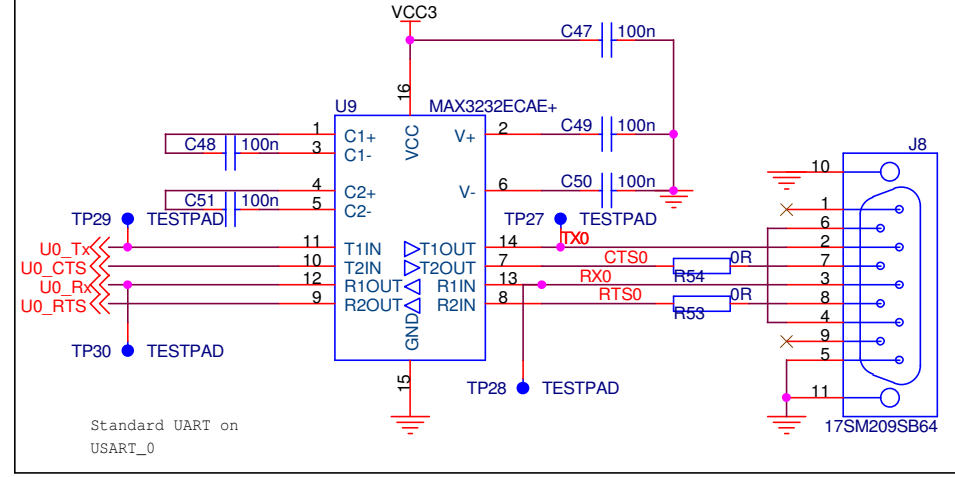
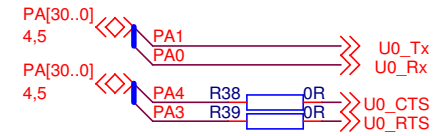


Pin	Direction	Signal	to connect to a modem
J9.1	Input	DCD	Data Carrier Detect
J9.2	Input	RXD	Received Data
J9.3	Output	TXD	Transmitted Data
J9.4	Output	DTR	Data Terminal Ready
J9.5	Signal Ground		
J9.6	Input	DSR	Data Set Ready
J9.7	Output	RTS	Request To Send
J9.8	Input	CTS	Clear To Send
J9.9	Input	RI	Ring Indicator



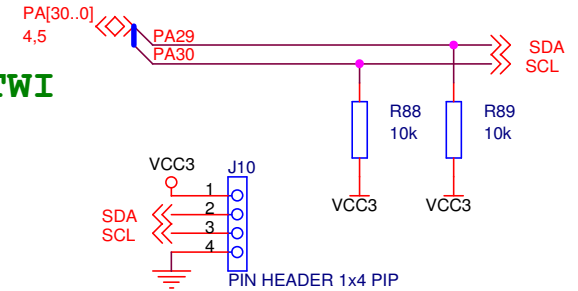
USART\_1 supports modem interface (UC3A as DTE)

### USART 0



Standard UART on USART\_0

### TWI



TWI Interface available on a a 4 pins footprint  
Do not cable any component.  
Header 4 footprint.

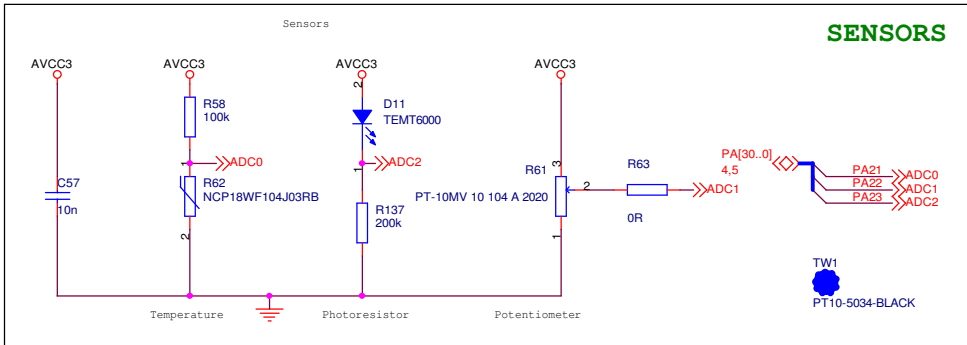
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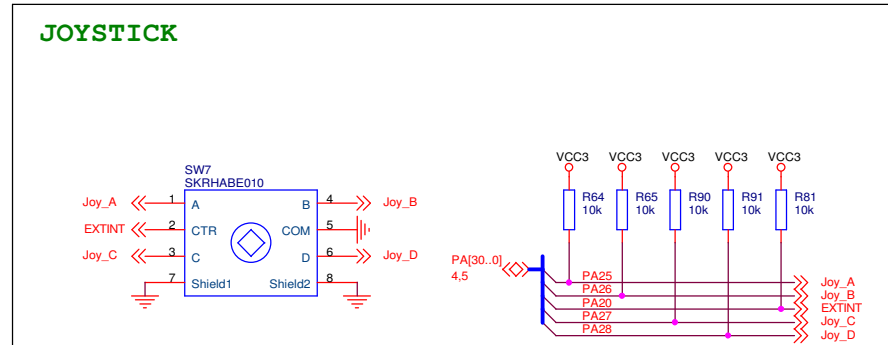


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A4	USART	C	
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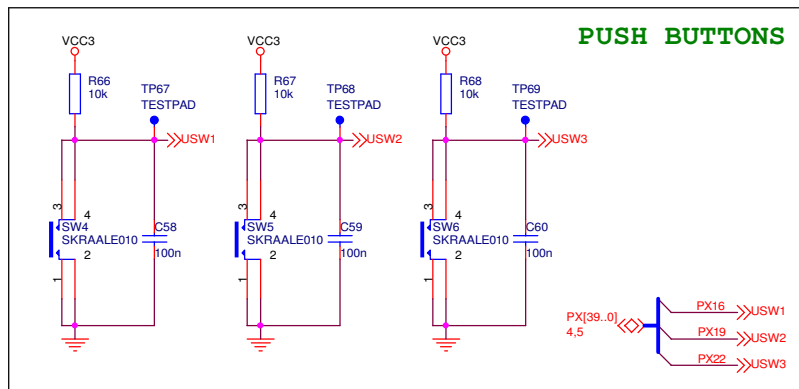
### SENSORS



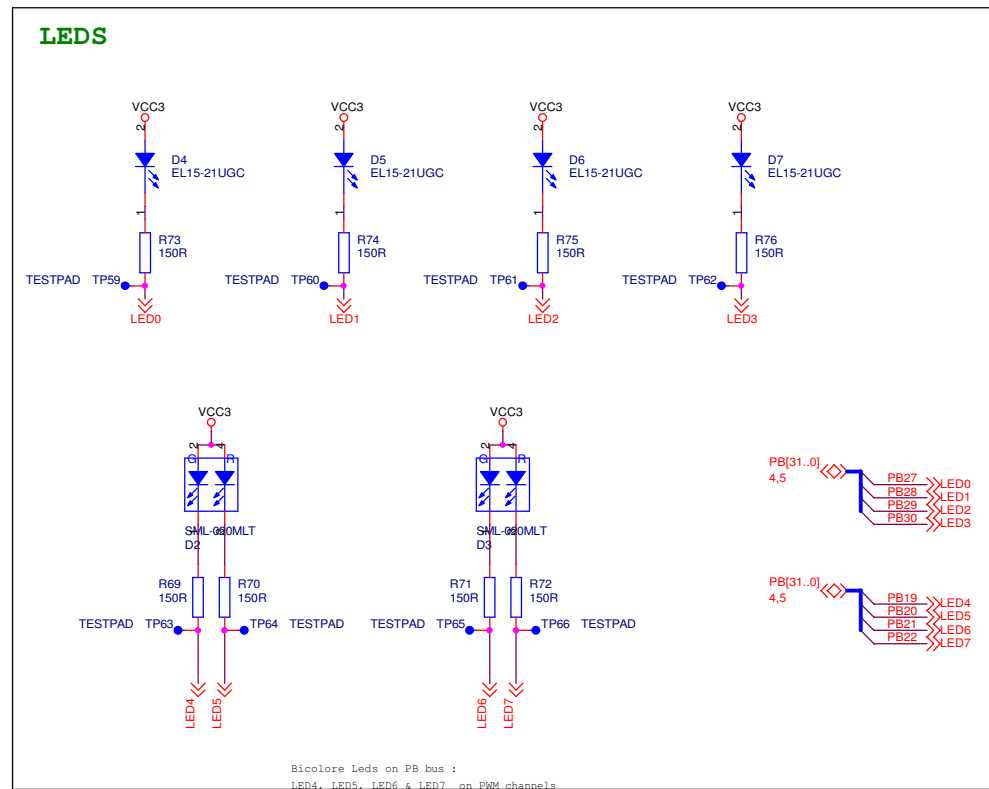
### JOYSTICK



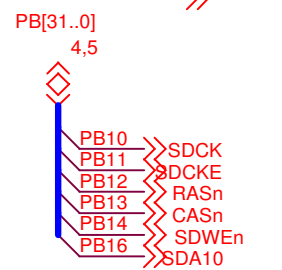
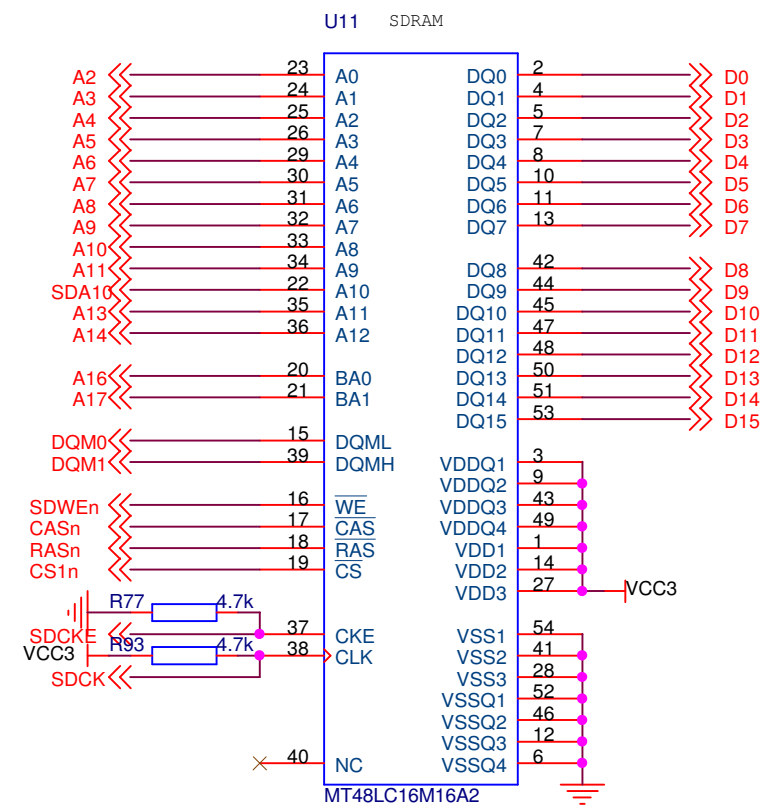
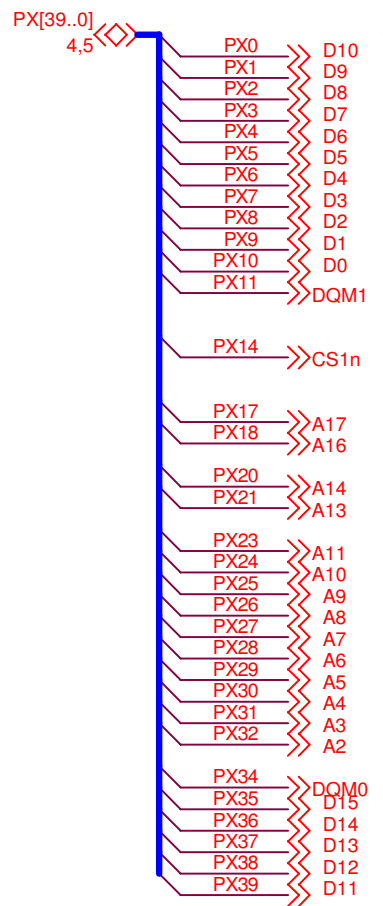
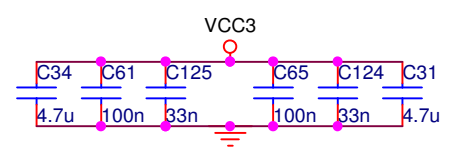
### PUSH BUTTONS



### LEDS



(NM : Not Mounted)

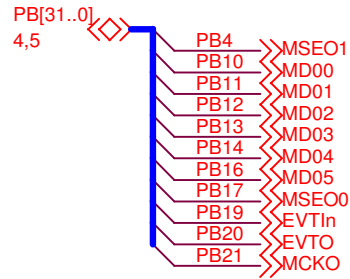


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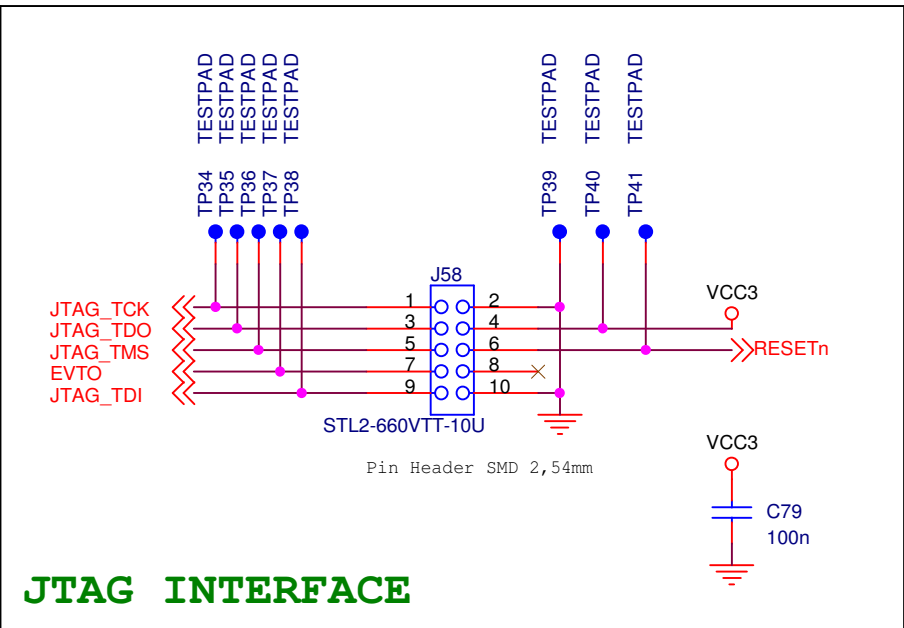
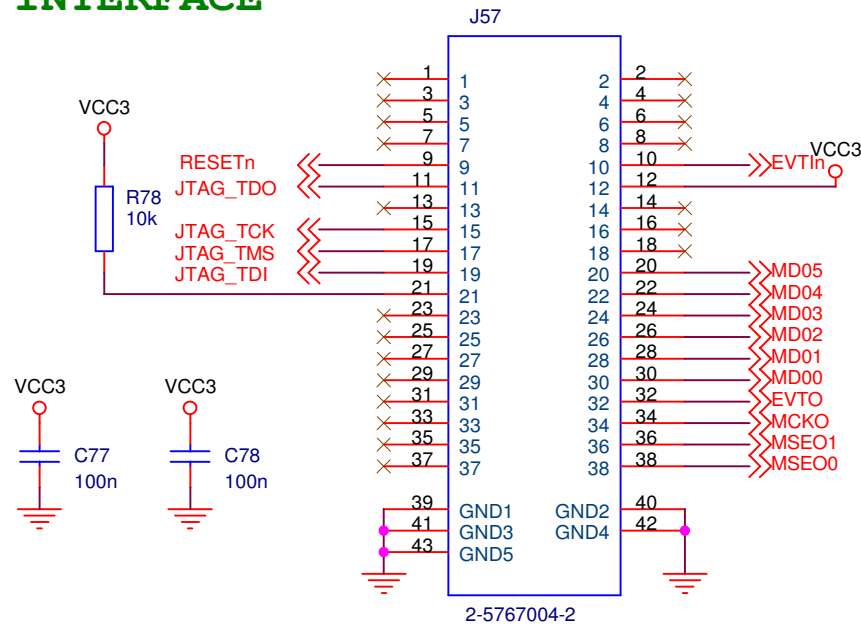
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Title EVK1100		
Size A	Document Number MEMORIES	Rev C
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(NM : Not Mounted)



### NEXUS INTERFACE



### JTAG INTERFACE

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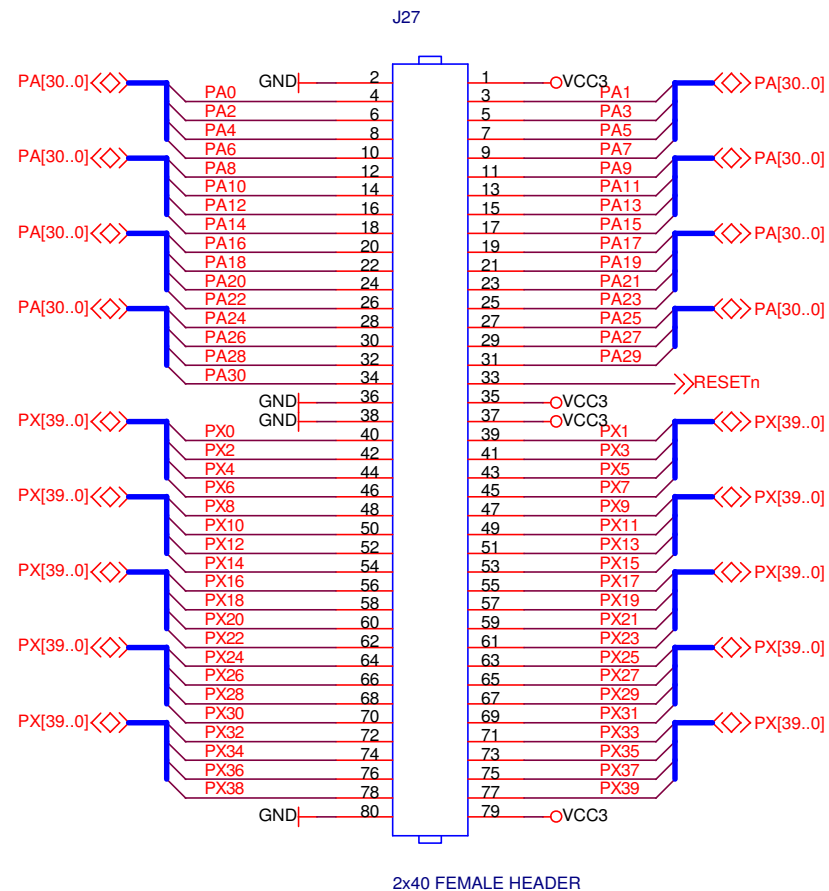
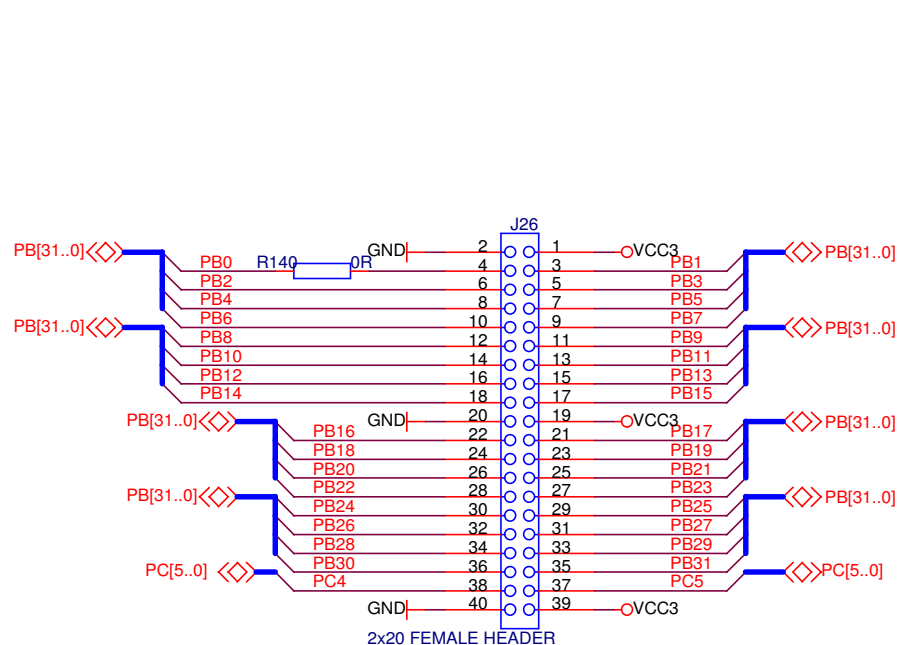
Title EVK1100

Size A Document Number MEMORIES  
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No component shall be implemented for this connector  
 Only a footprint of Header 1.27mm shall be present on PCB

This connector allows easy connection of peripheral in the prototyping area



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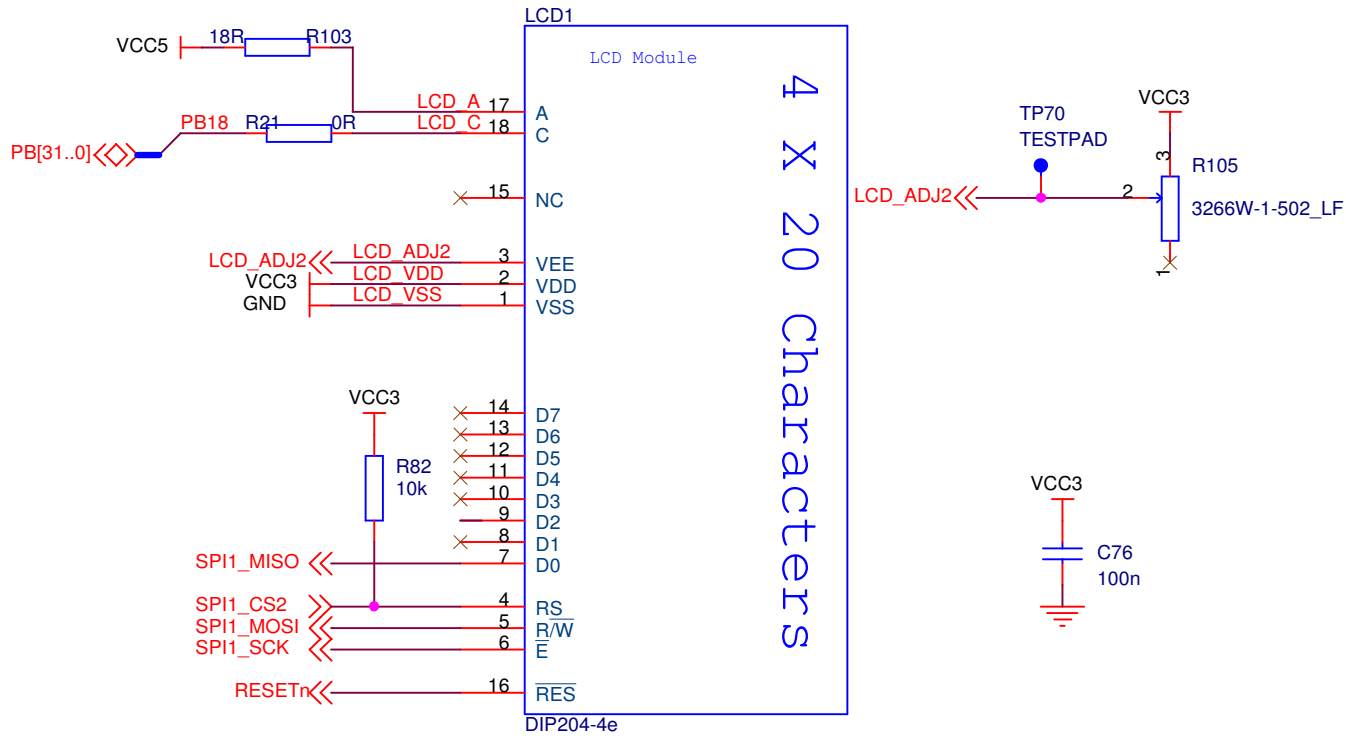
Size A4 Document Number

EXPANSION

Rev C

(NM : Not Mounted)

EA-DIP204-4 Blue white LCD  
KS0073 controller



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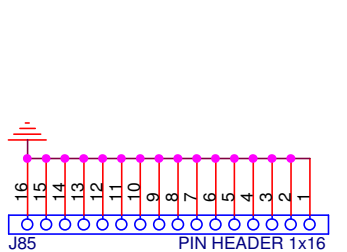
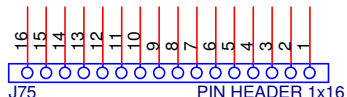
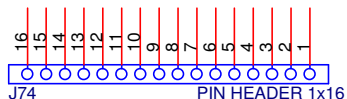
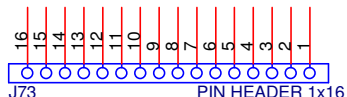
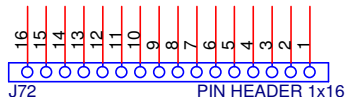
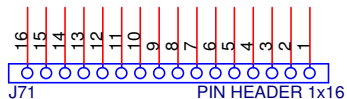
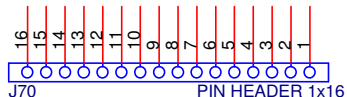
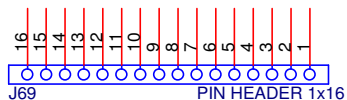
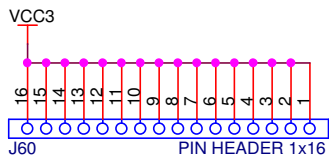
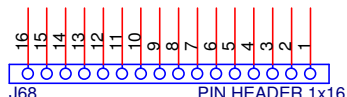
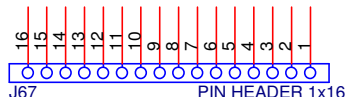
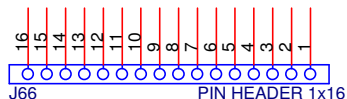
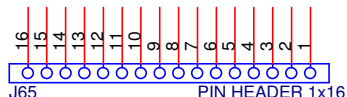
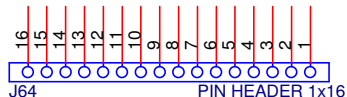
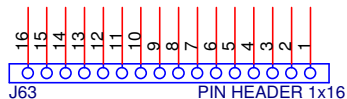
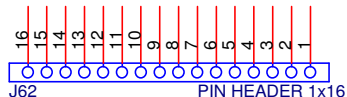
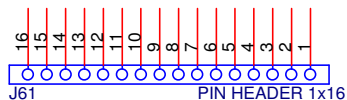


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24 lines of array  
 + 1 line of VCC  
 + 1 line of VSS



E13  
SJ-5076



E12  
SJ-5076



E11  
SJ-5076



E10  
SJ-5076



E3



E2



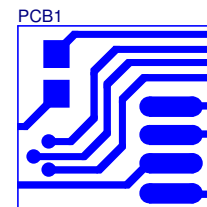
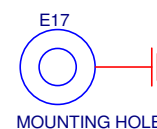
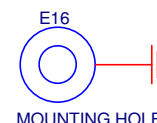
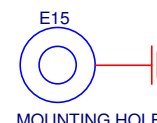
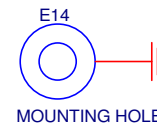
E1



E1



E1



A0609.3.1000.B

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Title EVK1100

Size A4	Document Number PROTO	Rev C
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ERRATA

REV C/

**USART 0**

U0\_CTS (PA.4) must be connected to U9.9  
U0\_RTS (PA.3) must be connected to U9.10  
U9.8 (R2IN) must be connected to J8.7  
U9.7 (T2OUT) must be connected to J8.8

**USART 1**

U10.11 (DTR1) must be connected to J9.6  
U10.8 (DSR1) must be connected to J9.4

**LEDS**

LED 5/Green is connected to GPIO PB.19 and not to PB.20  
LED 5/Red is connected to GPIO PB.20 and not to PB.19  
LED 6/Green is connected to GPIO PB.21 and not to PB.22  
LED 6/Red is connected to GPIO PB.22 and not to PB.21

**SPI**

Wrong Silkscreen : MISO and SCK are swapped

<Core Design>

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Title EVK1100

Size A Document Number ERRATA Rev C

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